

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Nabuo KARAKI

Application No.: 10/820,841

Docket No.: 119408

Filed: April 9, 2004

For: SEMICONDUCTOR INTEGRATED CIRCUIT, ELECTRONIC EQUIPMENT, AND
TRANSISTOR BACK-GATE VOLTAGE CONTROL METHOD

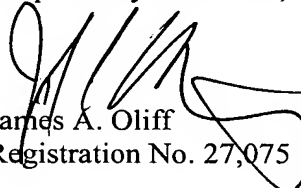
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,


James A. Oliff
Registration No. 27,075

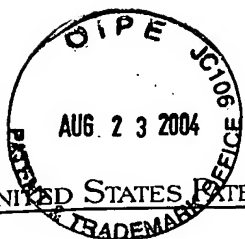
Joel S. Armstrong
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JAO:JSA/nxy

Date: August 23, 2004

OLIFF & BERRIDGE, PLC
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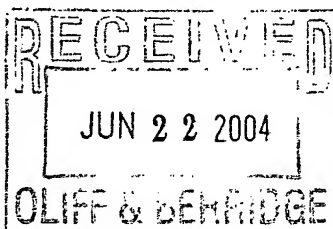


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/820,841	04/09/2004	2811	856	119408	8 ✓	19 ✓	4 ✓

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CONFIRMATION NO. 8476

FILING RECEIPT



OC000000013002609

Date Mailed: 06/21/2004

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Applicant(s)

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Assignment For Published Patent Application

Seiko Epson Corporation, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-112088 04/16/2003
 JAPAN 2004-044584 02/20/2004

If Required, Foreign Filing License Granted: 06/19/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor integrated circuit, electronic equipment, and transistor back-gate voltage control method

Preliminary Class

257

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